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U.S. UTILITY Patent Application

PATENT NUMBER and  
ISSUE DATE

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU
10081840	02/20/2002	268		2859

EXAMINER

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710 260 281

\*\*CONTINUING DATA VERIFIED:

\*\* FOREIGN APPLICATIONS VERIFIED:

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	
Foreign priority claimed 35 USC 119 conditions met		<input type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO
Verified and Acknowledged Examiners's initials		<input type="checkbox"/> yes <input type="checkbox"/> no	01-1016
TITLE : Methods, apparatus and software for improving SMI latency on systems using timing-sensitive registers			
U.S.DEP'T. OF COMMERCE/PAT. & TM-PTO-43BL(Rev 12-94)			

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
Amount Due	Date Paid		Total Claims	Print Claim for O.G
<input type="checkbox"/> TERMINAL DISCLAIMER		Primary Examiner	DRAWING	
			Sheets Drwg.	Fig.s Drwg.
		PREPARED FOR ISSUE		Application Examiner
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